<u>REMARKS</u>

The present invention is a method of updating a set of default coefficients used for quick convergence of an echo canceller. The echo canceller receives a reference signal and converges it to an estimated echo signal of an input signal according to a current set of filter coefficients via subtracting the estimated echo signal from the input signal to create a current error signal for output from the echo canceller and feedback of the current error signal as illustrated in new Fig. 3. The method includes applying the default coefficients to the echo canceller for generating a further echo signal; subtracting the further echo signal from the input signal to create a further error signal; and comparing the current error signal with a future error signal and in the event the further error signal exceeds the current error signal by a threshold amount, then replacing the set of default coefficients by the current set of filter coefficients.

Claim 2 has been amended to correct the typographical recitation of "amounts" to recite "amount" as suggested properly by the Examiner.

Furthermore, the Examiner's indication that claim 3 contains patentable subject matter is noted with appreciation. Claim 3 has been amended to be in independent form and further, new claim 4 has been added to depend from claim 3 which corresponds from the subject matter of claim 2.

Claims 1 and 2 stand rejected under 35 U.S.C. §102 as being anticipated by United States Patent 6,163,609 (Makinen et al). These grounds of rejection are traversed for the following reasons.

Claim 2 has been amended to recite the processing of the current error signal in Fig. 3 as being the output from the echo canceller. The Examiner's construction that current error signal is L_RES2 cannot be read upon claim 1 as amended wherein claim 1 recites that the echo canceller receives a reference signal and converges to an estimated echo signal of an input signal according to a current set of filter coefficients via subtracting said estimated echo signal from said input signal to create a current error signal from output of said echo canceller. Since the signal the Examiner has interpreted to be the current error signal is L_RES2, the recitation that the current error signal is the output of the echo canceller excludes

Makinen et al from anticipating or rendering obvious the subject matter of claim 1.

Accordingly, claim 1 as amended is patentable over the cited prior art.

Moreover, it is submitted that the amendment of claim 1 does not raise new issues and therefore should be entered placing the application in condition for allowance.

In view of the foregoing amendments and remarks, it is submitted that each of the claims in the application is in condition for allowance.

Accordingly, early allowance thereof is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 C.F.R. §1.136. Please charge any shortage in fees due in connection with the

filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (1375.43309X00) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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